



Serial No. 10/812,020

PATENT CLAIMS:

The text of all pending claims, (including withdrawn claims) is set forth below. Cancelled and not entered claims are indicated with claim number and status only. The claims as listed below show added text with underlining and deleted text with ~~strikethrough~~. The status of each claim is indicated with one of (original), (currently amended), (cancelled), (withdrawn), (new), (previously presented), or (not entered).

Please CANCEL claims 3 and 5, AMEND claims 9 and 18, and ADD claim 20 in accordance with the following:

1. (Original) A computer system, comprising:

a system memory;

a power management controller to control a supply power to the system;

a flash memory; and

a controller to enable a power saving standby mode, to control the power management controller to store an operating state stored in the system memory to the flash memory, and to cut power supply to the system when the power saving standby mode is selected.

2. (Original) The computer system according to claim 1, wherein the flash memory is connected to a universal serial bus port.

3. (Cancelled)

4. (Original) The computer system according to claim 1, wherein the controller stores the operating state stored in the flash memory to the system memory when the power saving standby mode is changed to a normal mode in which normal operations are conducted.

5. (Cancelled)

6. (Original) A control method of a computer system having a system memory and a power management controller to control a supply power to the system, comprising:
selecting a power saving standby mode;
storing an operating state stored in the system memory to a flash memory when the power saving standby mode is selected; and
cutting power supply to the system after the operating state has been stored.

7. (Original) The control method of the computer system according to claim 6, further comprising:

re-supplying power to the system when the power saving standby mode is changed to a normal mode in which normal operations are conducted; and
storing the operating state stored in the flash memory to the system memory.

8. (Original) The control method of the computer system according to claim 6, wherein the power saving standby mode is selected via a user interface.

9. (Currently Amended) The control method of the computer system according to claim 6, wherein the selection of the power saving standby mode comprises:

selecting a standby mode or a maximum power saving mode;
checking whether the flash memory is enabledconnected to the system; and
determining the selection of the standby mode or the maximum power saving mode as the selection of the power saving standby mode when the flash memory is connected.

10. (Original) The computer system according to claim 1, wherein the flash memory is detachably provided to the computer system.

11. (Original) The computer system according to claim 1, further comprising:
an operating system having a power management setup window via which the power saving standby mode is enabled.

12. (Original) The computer system according to claim 1, wherein a predetermined time is set to enter the power saving standby mode.

13. (Original) The computer system according to claim 2, wherein the controller controls the power management controller to copy the operating state stored in the flash memory to the system memory via the universal serial bus port when the power saving mode is changed to a normal mode.

14. (Original) A computer system having a system memory, comprising:
a power management controller to control a supply power to the computer system;

a flash memory; and

a controller to control the power management controller to store an operating state data stored in the system memory to the flash memory when a power saving standby mode is selected, to cut the power supply to the system, and to store the operating state to the system memory when a normal mode is selected.

15. (Original) The computer system according to 14, further comprising:

an operating system having a power management setup window via which the power saving standby mode is enabled.

16. (Original) The computer system according to claim 14, wherein the flash memory is connected to a universal serial bus port, and the operating state data is accessible when the flash memory is connected to the universal serial bus port.

17. (Original) The computer system according to claim 14, wherein storing of the operating state to the flash memory is performed in a basic input/output system of the computer system.

18. (Currently Amended) A method of controlling a computer system having a system memory and a power management controller to control a supply power to the system, comprising:

copying an operating state data stored in the system memory to a flash memory when a power saving standby mode of the computer system is activated; and

copying the operating state data back to the system memory when a normal mode of the computer system is activated.

19. (Original) The method according to claim 18, wherein the normal mode of the computer system is activated without a booting process.

20. (New) A computer system, comprising:

a system memory;

a power management controller to control a supply power to the system;

a flash memory; and

a basic input/output system of the computer system storing an operating state stored in

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the system memory to the flash memory and cutting power supply to the system, after being informed that the power saving standby mode is selected.